

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,298,745 B2
APPLICATION NO. : 10/015959
DATED : November 20, 2007
INVENTOR(S) : Egevang

Page 1 of 2

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 10, line 67, in Claim 1, delete "mi" and insert -- mi- --, therefor.

In column 11, line 2, in Claim 1, after "position;" insert -- sending a verification message once all entries in said verification table are filled; translating said first address into a second address, without reassembling said packet fragments into said packet, in response to said verification message; --.

In column 12, line 11, in Claim 11, delete "staffing" and insert -- starting --, therefor.

In column 12, line 11, in Claim 11, after "position" delete ":" and insert -- ; wherein said verifications module is to send a verification message to said translation module once all entries in said verification table are filled, and said translation module is to translate said first address into said second address, without reassembling said packet fragments into said packet, in response to said verification message. --, therefor.

In column 12, line 38, in Claim 13, after "position" delete ":" and insert -- ; wherein said intermediate node is further adapted to send a verification message once all entries in said verification table are filled and to translate said first address into said second address, without reassembling said packet fragments into said packet, in response to said verification message. --, therefor.

In column 12, line 66, in Claim 15, after "evaluate said" insert -- collected offset values to identify any missing positions between said starting position --.

In column 12, lines 66-67, in Claim 15, after "position" delete ":" and insert --; wherein said platform is further adapted to send a verification message once all entries in said verification table are filled and to translate said first address into said second address, without reassembling said packet fragments into said packet, in response to said verification message. --, therefor.

In column 13, line 17, in Claim 18, after "that" insert -- , when --.

In column 13, line 17, in Claim 18, after "computer" insert -- , --.

In column 13, line 28, in Claim 18, after "computer" insert -- , --.

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It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 14, line 8, in Claim 18, after "position" delete "." and insert -- ; wherein the stored instructions, when executed by a computer, further result in sending a verification message once all entries in said verification table are filled and translating said first address into said second address, without reassembling said packet fragments into said packet, in response to said verification message. --, therefor.

In column 14, line 10, in Claim 19, after "instructions" insert -- , when --.

In column 14, line 10, in Claim 19, after "computer" insert -- , --.

In column 14, line 19, in Claim 20, after "instructions" insert -- , when --.

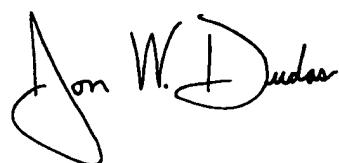
In column 14, line 19, in Claim 20, after "computer" insert -- , --.

In column 14, line 25, in Claim 21, after "instructions" insert -- , when --.

In column 14, line 25, in Claim 21, after "computer" insert -- , --

Signed and Sealed this

Twenty Second Day of April, 2008



JON W. DUDAS
Director of the United States Patent and Trademark Office